
Prof. Veljko Milutinovic

Life Member of the ACM

Fellow Member of the IEEE

Member of Academia Europaea

Member of the Serbian Academy of Engineering

Member of the Scientific Advisory Board of MindGenomics

Member of the Scientific Advisory Board of MaxelerTechnologies

About the Speaker:

Prof. Veljko Milutinovic received his PhD from the University of Belgrade, spent about a decade on various faculty positions in the USA (mostly at Purdue University), and was a codesigner of the DARPA's first GaAs RISC microprocessor. Since 1990 he taught and conducted research at the University of Belgrade, in ECE, MATH, MBA, and PHYS/CHEM. Now he serves on the Scientific Advisory Board of Maxeler Technologies in London and oversees the Belgrade operation of Maxeler as the Chairman of the Board of the Maxeler operation in Serbia. His research is mostly in datamining algorithms and dataflow computing, with the stress on mapping of data analytics algorithms onto fast energy efficient architectures. He also teaches a soft-skills course. For 7 of his books, forewords were written by 7 different Nobel Laureates with whom he cooperated on his past industry-sponsored projects. He has over 40 IEEE or ACM journal papers, over 400 Thompson-Reuters citations, and about 4000 Google-Scholar citations.

LIST OF PUBLICATIONS OF DR. VELJKO MILUTINOVIC

1. R. Trobec, R. Vasiljević, M. Tomašević, V. Milutinović, R. Beivide, and M. Valero, "Interconnection Networks in Petascale Computer Systems: A Survey," *ACM Computing Surveys*, DOI: 10.1145/2983387, Vol.49 (3), (September 2016), 24 pages, 2016, IF: 5,243 (M21a).
2. Z. Babovic, J. Protic, and V. Milutinovic, "Web Performance Evaluation for Internet of Things Applications," *IEEE Access*, DOI: 10.1109/ACCESS.2016.2615181, Vol. 4, pp. 6974 – 6992, 2016, IF: 1,270 (M22).
3. V. Blagojević, D. Bojić, M. Bojović, M. Cvetanović, J. Đorđević, Đ. Đurđević, B. Furlan, S. Gajin, Z. Jovanović, D. Milićev, V. Milutinović, B. Nikolić, J. Protić, M. Punt, Z. Radivojević, Ž. Stanisavljević, S. Stojanović, I. Tartalja, M. Tomašević, P. Vuletić, "A Systematic Approach to Generation of New Ideas for PhD Research in Computing," *Advances in Computers*, DOI: 10.1016/bs.adcom.2016.09.001, Vol. 104, 2016, IF: 0,303 (M23).
4. Z. Stanisavljevic, B. Nikolic, I.Tartalja, V. Milutinovic, "A classification of eLearning tools based on the applied multimedia", *Multimedia Tools and Applications*, DOI:10.1007/s11042-013-1802-4, Vol. 74, Issue 11, pp 3843-3880, June 2015, IF: 1,221 (M22).
5. I. Ratkovic, N. Bezanic, O. Ünsal, A. Cristal, V. Milutinovic, "Chapter One - An Overview of Architecture-Level Power- and Energy-Efficient Design Techniques," *Advances in Computers*, DOI: 10.1016/bs.adcom.2015.04.001, Vol. 98, pp. 1-57, 2015, IF: 0,302/2014 (M23).
6. Z. Sustran, G. Rakocevic, V. Milutinovic, "Dual Data Cache Systems: Architecture and Analysis", *Advances in Computers*, DOI:10.1016/bs.adcom.2014.11.001, Vol 96, pp. 187-233, 2015, IF: 0,302/2014 (M23).
7. A. Kos, S. Tomazic, J. Salom, N. Trifunovic, M. Valero, V. Milutinovic, "New Benchmarking Methodology and Programming Model for Big Data Processing," *International Journal of Distributed Sensor Networks*, DOI: 10.1155/2015/271752, Vol. 2015, 2015, IF: 0,665/2014 (M23).
8. G. Rakocevic, Z. Tafa, V. Milutinovic, "A Novel Approach to Data Mining in Wireless Sensor Networks," *Ad Hoc & Sensor Wireless Networks*, Vol. 22 (1-2), pp. 21-40, 2014, IF: 0,435 (M23).
9. M. Radulovic, M. Tomasevic, V. Milutinovic, "Register-Level Communication in Speculative Chip Multiprocessors", *Advances in Computers*, DOI:10.1016/B978-0-12-420232-0.00001-5, Vol, 92, pp. 1-66, 2014, IF: 0,302 (M23).
10. A. Vitorovic, M. Tomasevic, V. Milutinovic, "Manual Parallelization Versus State-of-the-Art Parallelization Techniques: The SPEC CPU2006 as a Case Study," *Advances in Computers*, DOI:10.1016/B978-0-12-420232-0.00005-2, Vol. 92, pp. 203-251, 2014, IF: 0,302 (M23).
11. B. Furlan, B. Nikolic, V. Milutinovic, "A Survey and Evaluation of State-of-the-Art Intelligent Question Routing Systems", *International Journal Of Intelligent Systems*, DOI: 10.1002/int.21597, Vol. 28, Issue 7, pp. 686–708, 2013, IF: 1.411 (M22).
12. M. Flynn, O. Mencer, V. Milutinovic, G. Rakocevic, P. Stenström, R. Trobec, M. Valero, "Moving from Petaflops to Petadata," *Communications of the ACM*, DOI: 10.1145/2447976.2447989, Vol. 56 (5), pp. 39-42, 2013, IF:2,863 (M21a).
13. Z. Babovic, V. Milutinovic, "Novel System Architectures for Semantic-Based Integration of Sensor Networks," *Advances in Computers*, DOI:10.1016/B978-0-12-408091-1.00002-6, Vol. 90, pp. 91-183, 2013, IF:0,489 (M23).
14. S. V. Stankovic, N. Kojic, G. Rakocevic, D. Vitas, V. Milutinovic, "A Classification of Data Mining Algorithms for Wireless Sensor Networks, and Classification Extension to Concept Modeling in System of Wireless Sensor Networks Based on Natural Language Processing," *Advances in Computers*, DOI:10.1016/B978-0-12-408091-1.00004-X, Vol. 90, pp. 223-283, 2013, IF:0,489 (M23).

15. Z. Jovanovic, V. Milutinovic, "FPGA accelerator for floating-point matrix multiplication, " *IET Computers & Digital Techniques*, DOI:10.1049/iet-cdt.2011.0132, Vol. 6(4), pp. 249-256, 2012, IF: 0,284 (M23).
16. S. Tomazic, V. Pavlovic, J. Milovanovic, J. Sodnik, A. Kos, S. Stancin, V. Milutinovic, "Fast file existence checking in archiving systems," *ACM Transactions on Storage*, DOI:10.1145/1970343.1970345, Vol. 7(1), 2011, IF:1,115/2012 (није имао импакт фактор у години у којој је рад објављен, али јесте у следећој години) (M22).
17. Z. Tafa, G. Rakocevic, D. Mihailovic, V. Milutinovic, "Effects of Interdisciplinary Education on Technology-Driven Application Design, " *IEEE Transactions on Education*, DOI:10.1109/TE.2010.2080359, Vol. 54(3), pp. 462-470, 2011, IF: 1,021 (M22).
18. S. Omerovic, Z. Babovic, Z. Tafa, V. Milutinovic, S. Tomazic, "Concept modeling: From origins to multimedia," *Multimedia Tools and Applications*, DOI:10.1007/s11042-010-0642-8, Vol. 51(3), pp. 1175-1200, 2011, IF:0,617 (M23).
19. Z. Tafa, V. Milutinovic, "Detectability of Static and Moving Targets in Randomly Deployed Military Surveillance Networks," *Ad Hoc & Sensor Wireless Networks*, Vol. 13(3-4), pp. 291-312, 2011, IF: 0,400 (M23).
20. S. Djordjevic, S. Stancin, A. Meglic, V. Milutinovic, S. Tomasic, "MC Sensor-A Novel Method for Measurement of Muscle Tension," *Sensors*, DOI: 10.3390/s111009411, Vol. 11 (10), pp. 9411-9425, 2011, IF: 1,739 (M21).
21. S. Omerovic, S. Tomazic, M. Milutinovic, V. Milutinovic, "Methodology for Written and Oral Presentation of Research Results," *Journal of Professional Issues In Engineering Education and Practice*, Vol 136 (2), pp. 112-117, 2010, IF: 0,372 (M23).
22. B. Nikolic, Z. Radivojevic, J. Djordjevic, V. Milutinovic, "A Survey and Evaluation of Simulators Suitable for Teaching Courses in Computer Architecture and Organization," *IEEE Transaction on Education*, Vol. 52(4), pp. 449-458, 2009, IF: 0,822 (M22).
23. V. Milutinovic, "Our Profession Needs a Reminder," *IEEE Computer*, Vol. 39(5), pp. 102-104, 2006, IF: 1,289 (M21).
24. J. Sahuquillo, S. Petit, A. Pont, V. Milutinovic, "Exploring the performance of split data cache schemes on superscalar processors and symmetric multiprocessors," *Journal of Systems Architecture*, Vol. 51(8), pp. 451-469, 2005, IF:0,402 (M23).
25. N. Milanovic, M. Malek, A. Davidson, V. Milutinovic, "Routing and Security in Mobile Ad Hoc Networks," *IEEE Computer*, Vol. 37(2), pp. 61-65, 2004, IF: 1,432 (M21).
26. V. Milutinovic et al. "Testing the E-Business Infrastructure: Expanding into the Wireless/Mobile Environments," *Telecommunication Systems*, Vol. 22(1-4), pp. 141-150, 2003, IF: 0,413 (M23).
27. V. Milutinovic, N. Skundric, "Will Distance Learning Create a Global University," *IEEE Computer*, Vol. 36(3), pp. 98-100, 2003, IF: 1,552 (M21).
28. M. Kovacevic, M. Diligenti, M. Gori, V. Milutinovic, "Recognition of Common Areas in a Web Page Using a Visualization Approach," in *Artificial Intelligence: Methodology, Systems, and Applications, Lecture Notes in Computer Science*, Vol. 2443, pp. 203-212, 2002, IF: 0,515 (M23).
29. E. Jovanov, V. Milutinovic, A. Hurson, "Acceleration of Nonnumeric Operations Using Hardware Support for the Ordered Table Hashing Algorithms," *IEEE Transactions on Computers*, Vol. 51(9), pp. 1026-1040, 2002, IF: 1,484 (M21).

30. A. Milenkovic, V. Milutinovic, "A performance evaluation of cache injection in bus-based shared memory multiprocessors," *Microprocessors and Microsystems*, Vol. 26(2), pp. 51-61, 2002, IF: 0,341 (M23).
31. D. Horvat, D. Cvetkovic, V. Milutinovic, P. Kocovic, V. Kovacevic, "Mobile Agents and Java Mobile Agents Toolkits," *Telecommunication Systems*, Vol. 18(1-3), pp. 271-287, 2001, IF: 0,109 (M23).
32. D. Cvetkovic, M. Pesic, D. Petkovic, V. Milutinovic, D. Horvat, P. Kocovic, V. Kovacevic, "Architecture of the Mobile Environment for Intelligent Genetic Search and Proxy Caching," *Telecommunication Systems*, Vol. 18(1-3), pp. 255-270, 2001, IF: 0,109 (M23).
33. A. Ngom, I. Stojmenovic, V. Milutinovic, "STRIP - a strip-based neural-network growth algorithm for learning multiple-valued functions," *IEEE Transactions on Neural Networks*, Vol. 12(2), pp. 212-227, 2001, IF: 1,479 (M21).
34. J. Sahuquillo, A. Pont, V. Milutinovic, "The Filter Data Cache: A Tour Management Comparison with Related Split Data Cache Schemes Sensitive to Data Localities," *Lecture Notes in Computer Science*, Vol. 1940, pp. 319-327, 2001, IF: 0,415 (M23).
35. V. Milutinovic, P. Knezevic, B. Radunovic, S. Casselman, J. Schewel, "Obelix Searches Internet Using Customer Data," *IEEE Computer*, Vol. 33(7), pp. 104-107, 2000, IF: 1,043 (M21).
36. V. Milutinovic, D. Cvetkovic, J. Mirkovic, "Genetic Search Based on Multiple Mutations," *IEEE Computer*, Vol. 33(11), pp. 118-119, 2000, IF: 1,043 (M21).
37. V. Milutinovic, "Caching in distributed systems," *IEEE Concurrency*, Vol. 8(3), pp. 14-15, 2000, IF: 1,018 (M21).
38. M. Jovanovic, V. Milutinovic, "An overview of reflective memory systems," *IEEE Concurrency*, Vol. 7(2), pp. 56-64, 1999, IF: 0,784 (M21).
39. V. Milutinovic, M. Valero, "Enhancing and Exploiting the Locality," *IEEE Transactions on Computers*, Vol. 48(2), pp. 97-99, 1999, IF: 1,057 (M21).
40. A. Milenkovic, V. Milutinovic, "A quantitative analysis of wiring lengths in 2D and 3D VLSI," *Microelectronics journal*, Vol. 29 (6), pp. 313-321, 1998, IF: 0,345 (M22).
41. G. Davidovic, J. Ciric, V. Milutinovic, G. Hadzic, L. Radicevic, "VLSI implementation of detection of R2 signalization," *Microprocessors and Microsystems, Embedded Hardware Design*, Vol. 21(2), pp. 73-78, 1997, IF: 0,163 (M23).
42. I. Tartalja, V. Milutinovic, "Classifying Software-Based Cache Coherence Solutions," *IEEE Software*, Vol. 14(3), pp. 90-101, 1997, IF: 0,768 (M21).
43. D. Milutinovic, V. Milutinovic, "New solutions for new technologies," *IEEE Computer*, Vol. 29(4), 1996, IF:0,608/1997 (M21).
44. A. Grujic, M. Tomasevic, V. Milutinovic, "A simulation study of hardware-oriented DSM approaches," *IEEE Parallel & Distributed Technology Systems*, Vol. 4(1), pp. 74-83, 1996, IF: 0,574/1997 (M21).
45. J. Protic, M. Tomasevic, V. Milutinovic, "Distributed shared memory: concepts and systems," *IEEE Parallel & Distributed Technology Systems*, Vol. 4(2), pp. 63-71, 1996, IF: 0,574/1997 (M21).
46. M. Tomasevic, V. Milutinovic, "The word-invalidate cache coherence protocol," *Microprocessors and Microsystems - Embedded Hardware Design*, Vol. 20(1), pp. 3-16, 1996, IF: 0,163/1997 (M23).
47. S. Lakhani, Y. Wang, A. Milenković, V. Milutinović, "2D matrix multiplication on a 3D systolic array," *Microelectronics journal*, Vol. 27(1), pp. 11-22, 1996, IF: 0,227/1997 (M23).

48. I. Ekmecic, I. Tartalja, V. Milutinovic, "A survey of heterogeneous computing: concepts and systems," *Proceedings of the IEEE*, Vol. 84 (8), pp. 1127-1144, 1996, IF: 2,699/1997 (M21a).
49. V. Milutinovic, Z. Petkovic, "Ten Lessons Learned from a RISC Design," *IEEE Computer*, Vol. 28(3), pp. 120, 1995, IF: 0,608/1997 (M21).
50. I. Ekmecic, I. Tartalja, V. Milutinovic, "EM³: A Taxonomy of Heterogeneous Computing Systems," *IEEE Computer*, Vol. 28(12), pp. 68-70, 1995, IF: 0,608/1997 (M21).
51. S. Savic, M. Tomasevic, V. Milutinovic, A. Gupta, M. Natale, I. Gertner, "Improved RMS for the PC environment," *Microprocessors and Microsystems - Embedded Hardware Design*, Vol. 19(10), pp. 609-619, 1995, IF: 0,163/1997 (M23).
52. M. Tomasevic, V. Milutinovic, "Hardware approaches to cache coherence in shared-memory multiprocessors, Part 1," *IEEE Micro*, Vol. 14(5), pp. 52-59, 1994, IF: 0,992/1997 (M21).
53. M. Tomasevic, V. Milutinovic, "Hardware approaches to cache coherence in shared-memory multiprocessors 2.," *IEEE Micro*, Vol. 14(6), pp. 61-66, 1994, IF: 0,992/1997 (M21).
54. B. Perunicic, S. Lakhani, V. Milutinovic, "Stochastic Modeling and Analysis of Propagation Delays in GaAs Adders," *IEEE Transactions on Computers*, Vol. 40(1), pp. 31-45, 1991, IF: 1,208/1992 (M21a).
55. V. Milutinovic, D. Fura, W. Helbig, "Pipeline Design Tradeoffs in a 32-bit Gallium Arsenide Microprocessor," *IEEE Transactions on Computers*, Vol. 40(11), pp. 1214-1224, 1991, IF: 1,208/1992 (M21a).
56. W. Helbig, V. Milutinovic, "A DCFL E/D-MESFET GaAs Experimental RISC Machine," *IEEE Transactions on Computers*, Vol. 38(2), pp. 263-274, 1989, IF: 1,388/1988 (M21).
57. V. Milutinovic, M. Bettinger, W. Helbig, "Multiplier/Shifter Design Tradeoffs in a 32-bit Microprocessor," *IEEE Transactions on Computers*, Vol. 38(6), pp. 874-881, 1989, IF: 1,388/1988 (M21).
58. V. Milutinovic, "Microprocessor architecture and design for gallium arsenide technology (part 2)," *Microelectronics Journal*, Vol. 20(3), pp. 7-9, 1989.
59. P. Chow, T. Geigel, V. Milutinovic, J. Pridmore, "Impact of mapping parameters on the performance of small cache memories," *Microprocessors and Microsystems - Embedded Hardware Design*, Vol. 12(4), pp. 197-205, 1988, IF: 0,404 (M22).
60. A. Kabakibo, V. Milutinovic, "Simulation study of the impact of technology on cache memory performance," *Microprocessors and Microsystems - Embedded Hardware Design*, Vol. 12(5), pp. 277-285, 1988, IF: 0,404 (M22).
61. V. Milutinovic, "Microprocessor architecture and design for GaAs technology," *Microelectronics Journal*, Vol. 19(4), pp. 51-55, 1988.
62. H. Vlahos, V. Milutinovic, "GaAs microprocessors and digital systems: an overview of R&D efforts," *IEEE Micro*, Vol. 8(1), pp. 28-56, 1988, IF: 0,595 (M22).
63. V. Milutinovic, "A comparison of suboptimal detection algorithms applied to the additive mix of orthogonal sinusoidal signals," *IEEE Transactions on Communications*, Vol. 36(5), pp. 538-543, 1988, IF: 0,802 (M21).
64. V. Milutinovic, J. Crnkovic, C. Houstis, "A Simulation Study of Two Distributed Task Allocation Procedures," *IEEE Transactions on Software Engineering*, Vol. 14(1), pp. 54-61, 1988, IF: 1,387 (M21).
65. C. Gimarc, V. Milutinovic, "A Survey of RISC Processors and Computers of the Mid-1980s," *IEEE Computer*, Vol. 20(9), pp. 59-69, 1987, IF: 1,096 (M21).

66. V. Milutinovic, D. Fura, W. Helbig, J. Linn, "Architecture/Compiler Synergism in GaAs Computer Systems," IEEE Computer, Vol. 20(5), pp. 72-93, 1987, IF: 1,096 (M21).
67. D. Milutinovic, V. Milutinovic, B. Soucek, "The honeycomb architecture," IEEE Computer, Vol. 20 (4), pp. 81-83, 1987, IF: 1,096 (M21).
68. B. Furht, V. Milutinovic, "A Survey of Microprocessor Architectures for Memory Management," IEEE Computer, Vol. 20(3), pp. 48-67, 1987, IF: 1,096 (M21).
69. K. McNeley, V. Milutinovic, "Emulating a Complex Instruction Set Computer with a Reduced Instruction Set Computer," IEEE Micro, Vol. 7(1), pp. 60-72, 1987, IF: 0,392 (M23).
70. V. Milutinovic, N.é Lopez-Benitez, K. Hwang, "A GaAs-Based Microprocessor Architecture for Real-Time Applications," IEEE Transactions on Computers, Vol. 36(6), pp. 714-727, 1987, IF: 1,517 (M21).
71. V. Milutinovic, "A Simulation Study of the Vertical-Migration Microprocessor Architecture," IEEE Transactions on Software Engineering, Vol. 13(12), pp. 1265-1277 1987, IF: 1,402 (M21).
72. V. Milutinovic, "Guest Editor's Introduction GaAs Microprocessor Technology," IEEE Computer, Vol. 19(10), pp. 10-13, 1986, IF: 1,111 (M21).
73. V. Milutinovic et al. "Issues of Importance in Designing GaAs Microcomputer Systems," IEEE Computer, Vol. 19(10), pp. 45-57, 1986, IF: 1,111 (M21).
74. A. Silbey, V. Milutinovic, V. Mendoza-Grado, "A Survey of Advanced Microprocessors and HLL Computer Architectures," IEEE Computer, Vol. 19(8), pp. 72-85, 1986, IF: 1,111 (M21).
75. V. Milutinovic, D. Fura, W. Helbig, "An Introduction to GaAs Microprocessor Architecture for VLSI," IEEE Computer, Vol. 19(3), pp. 30-42, 1986, IF: 1,111 (M21).
76. V. Milutinovic, J. A. B. Fortes, L. Jamieson, "A multimicroprocessor architecture for real-time computation of a class of DFT algorithms," IEEE Transactions on Acoustics, Speech, and Signal Processing, Vol. 34(5), pp. 1301-1309, 1986, IF: 1,032 (M22).
77. Veljko Milutinović, "A microprocessor-oriented algorithm for adaptive equalization," IEEE Transactions on Communications, Vol. 33(6), pp. 522-526, 1985, IF: 0,983 (M21).
78. V. Milutinovic, "Performance comparison of two suboptimum detection procedures in real environment," IEE Proceedings F. Communications, Radar and Signal Processing, Vol. 131(4), pp. 341-344, 1984.
79. V. Milutinovic, "Comparison of three suboptimum detection procedures," Electronics Letters, Vol. 16(17), pp. 681-683, 1980, IF: 1,345/1981 (M21).
80. V. Milutinovic, "Suboptimum detection procedure based on the weighting of partial decisions," Electronics Letters, Vol. 16(6), pp. 237-238, 1980, IF: 1,345/1981 (M21).

PERSONAL PROFILE FORM (ED)

Type of appointment: [X] Academic (Includes Resident Interns) [] Student Academic [] Staff [] Hourly

Direct deposit is mandatory for all IU employees. To enroll in direct deposit, visit www.fms.iu.edu/payroll/direct_bank_deposit.asp. If you do not enroll prior to the first paycheck, all payments will be deposited onto a pay card until enrollment is received. This pay card can be obtained from the campus Payroll office.

[] Current or former Employee/Student University ID# _____ (10-digit Employee ID)
Last four digits of Social Security Number _____

[X] No previous association with Indiana University Social Security Number: 591 - 14 - 2655

Legal Name Milutinovic Veljko
Last First Middle Suffix

Note: Legal name must match that as recorded by the Social Security Administration (SSA). If your name is not correct with SSA, you must update your records with that office.

Preferred Name
Last First Middle Suffix

Passport Name Milutinovic Veljko
(Non-U.S. Citizens only) Last First Middle Suffix

Birth Date 05 / 04 / 1951
Month / Day / Year

Gender [X] Male [] Female

Marital Status [] Single [X] Married

US Status - MUST choose one. [] U.S. Citizen [X] Lawful Permanent Resident [] Other

If not U.S. Citizen, primary country of citizenship Serbia

Home Address 11860 Tidewater Drive 929 - 321 - 9081
(your legal residence) Street (Apt#) Phone
Fishers Indiana 46037 USA
City State Zip Code Country

The home address (your legal residence) is necessary for the university to mail tax information (including W2s) and benefit enrollment information (for eligible employees). It is also required for tax reporting to the IRS.

Non-IU email address vm@etf.rs

Ethnic Group

Are you Hispanic or Latino? [] Yes [X] No

What is your race? Select one or more.

- [] American Indian or Alaska Native: A person having origins in any of the original peoples of North America, and who maintain cultural identification through tribal affiliation or community recognition
[] Asian: A person having origins in any of the original peoples of the Far East, Southeast Asia, or the Indian subcontinent including, for example, Cambodia, China, India, Japan, Korea, Malaysia, Pakistan, the Philippine Islands, Thailand, and Vietnam
[] Black/African American: A person having origins in any of the Black racial groups of Africa
[] Native Hawaiian or Other Pacific Islander: A person having origins in any of the original peoples of Hawaii, Guam, Samoa, or other Pacific Islands
[X] White: A person having origins in any of the original peoples of Europe, North Africa, or the Middle East

Military Status

No Military Service

Disabled Veteran: (i) A veteran of the U.S. military, naval or air service who is entitled to compensation (or who but for the receipt of military retired pay would be entitled to compensation) under laws administered by the Secretary of Veterans Affairs, or (ii) a person who was discharged or released from active duty because of a service-connected disability.

Recently Separated Veteran: Any veteran during the three-year period beginning on the date of such veteran's discharge or release from active duty in the U.S. military, ground, naval, or air service.

Other Protected Veteran: A veteran who served on active duty in the U.S. military, ground, naval, or air service during a war or in a campaign or expedition for which a campaign badge has been authorized, under the laws administered by the Department of Defense.

Armed Forces Service Medal Veteran: Any veteran who, while serving on active duty in the U.S. military, ground, naval, or air service, participated in a United States military operation for which an Armed Forces service medal was awarded pursuant to Executive Order 12985.

Signature  Date 11/15/17

Confidentiality - Under federal law Indiana University is required to collect and report data regarding the gender, racial and ethnic composition and veteran status of its workforce. This information is used for reporting and administrative purposes.

Departments:

After the hiring process is complete, please shred this form. Additionally, please note that:

- Academic employees must complete the PS and PSA personal profile forms and submit to the campus Academic Affairs office.
- Staff employees must complete the PSA personal profile form and submit to the campus Human Resources office.